



AF/GAM-2787

# 11/B

(B#)  
4-26-00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

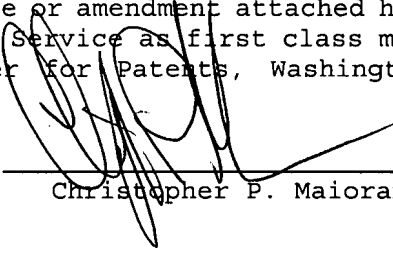
TO 2700 MAIL ROOM

APR 25 2000

RECEIVED

Application of: Krishna Rangasayee et al.  
Serial No.: 08/707,694  
Title: PROGRAMMABLE LOGIC DEVICE HAVING  
INTEGRATED PHASE LOCK LOOP  
Filed: September 4, 1996  
Attorney Docket No.: 0325.00063  
Examiner: Butler, D.  
Art Unit: 2787  
In Response To: Office Action mailed February 1, 2000

I hereby certify that this letter, the response or amendment attached hereto are being deposited with the United States Postal Service as first class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, D.C. 20231, on April 14, 2000.

By:   
Christopher P. Maiorana

AMENDMENT AFTER FINAL

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In response to the Office Action mailed February 1, 2000,  
please amend the above-identified application as follows:

IN THE CLAIMS

Please cancel claims 1, 11, 13 and 14 without prejudice.

Please amend the claims as follows:

B

Do Not  
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DMB  
4-27-00